



PTO/SB/08A (07-05)

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**Complete if Known****INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 1 of 2

Application Number	10/691,080
Filing Date	October 22, 2003
First Named Inventor	Barry E. Burke
Art Unit	2818
Examiner Name	C. Luu
Attorney Docket Number	MIT8431L

**U. S. PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
CPL	A1	US- 3,728,590	04-17-1973	Kim et al.	
	A2	US- 3,853,634	12-10-1974	Amelio et al.	
	A3	US- 5,298,448	03-29-1994	Stevens et al.	
	A4	US- 4,063,992	12-20-1977	Hosack	
	A5	US- 4,613,402	09-23-1986	Losee et al.	
	A6	US- 5,302,544	04-12-1994	Lavine	
	A7	US- 5,314,836	05-24-1994	Lavine	
	A8	US- 5,556,801	09-17-1996	Hawkins et al.	
	A9	US- 5,606,187	02-25-1997	Bluzer et al.	
	A10	US- 5,719,075	02-17-1998	Hawkins et al.	
	A11	US- 5,760,431	06-02-1998	Savoye et al.	
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**FOREIGN PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	7 <sup>6</sup>
		Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)				

Examiner  
SignatureDate  
Considered

10/27/05

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. <sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language Translation is attached.

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Sheet	2	of	2	Attorney Docket Number	MIT8431L

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
CAL	B1	BROWNE et al., "A Nonoverlapping Gate Charge-Coupling Technology for Serial Memory and Signal Processing Applications," IEEE Jnl of Solid-State Circuits, Vol. SC11, No. 1, pp. 203-207, February 1976.	
	B2	KAPOOR, "Charge-Coupled Devices with Submicron Gaps," IEEE Electron Device Ltrs., Vol. EDL-2, No. 4, pp. 92-94, April 1981.	
	B3	SLOTBOOM et al., "Submicron CCD memory structures fabricated by electron-beam lithography," Proc. IEDM '84, paper 11.6, pp. 308-311, 1984.	
	B4	GAJAR et al., "An ionic liquid-channel field-effect transistor," J. Electrochem. Soc., V. 139, No. 10, pp. 2833-2840, October 1992.	
	B5	YAMADA et al., "Driving Voltage Reduction in a Two-Phase CCD by Suppression of Potential Pockets in Inter-Electrode Gaps," IEEE Trans. on Electron Devices, V. 44, No. 10, pp. 1580-1587, October 1997.	
	B6	OKADA et al., "Performance of FT-CCD image sensor with Single Layer Poly-Silicon Electrode," 1999 IEEE Workshop on Charge-Coupled Devices and Advanced Imager Sensors, pp. 219-222, Nagano, Japan, June 1999.	
CAL	B7	BURKE et al., "Charge-coupled device made with a single polysilicon level," Massachusetts Institute of Technology Lincoln Laboratory Solid State Research Quarterly Technical Report, pp. 31-37, March 1, 2002.	

Examiner Signature		Date Considered	10/27/05
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